

Fig. 1

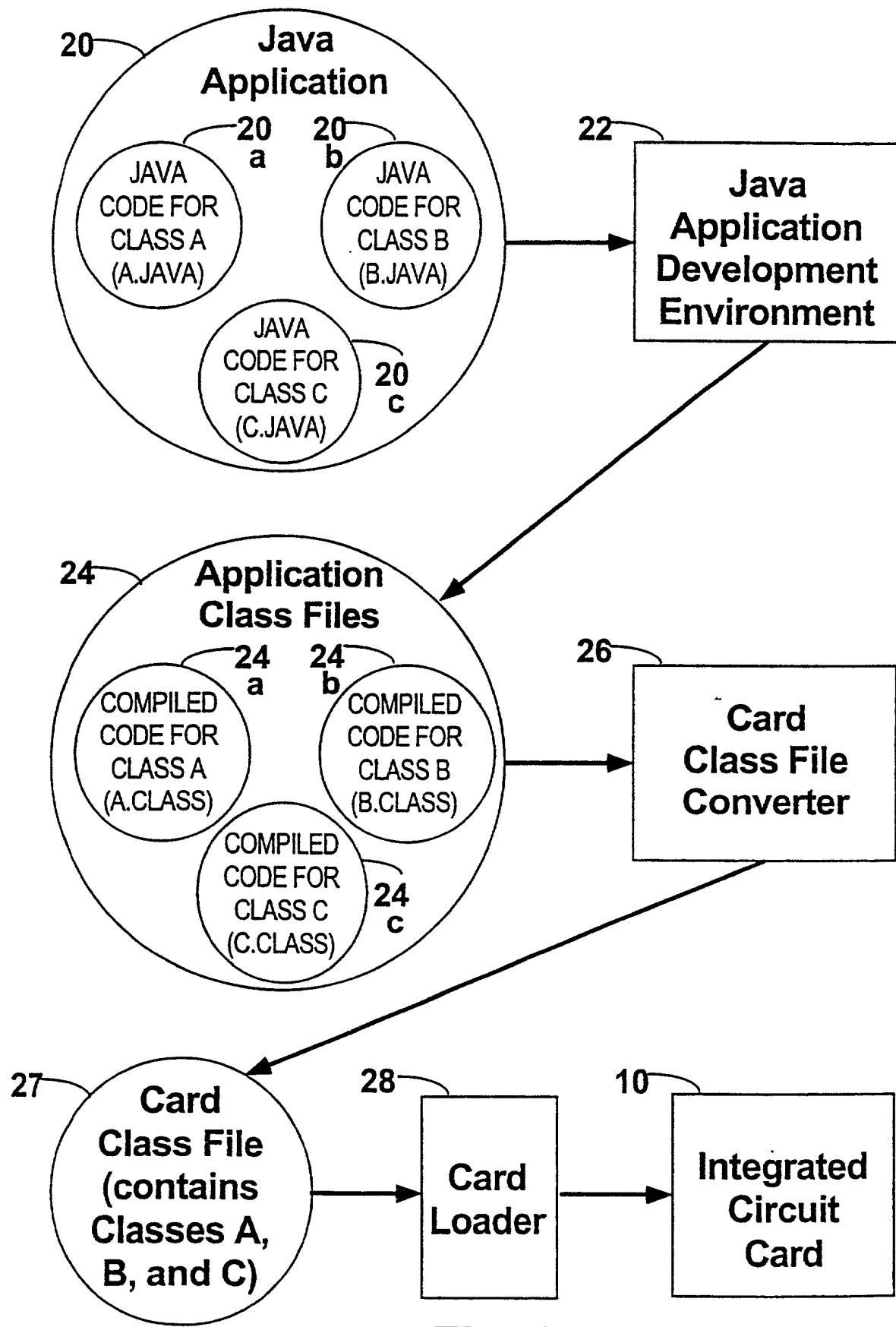


Fig. 2

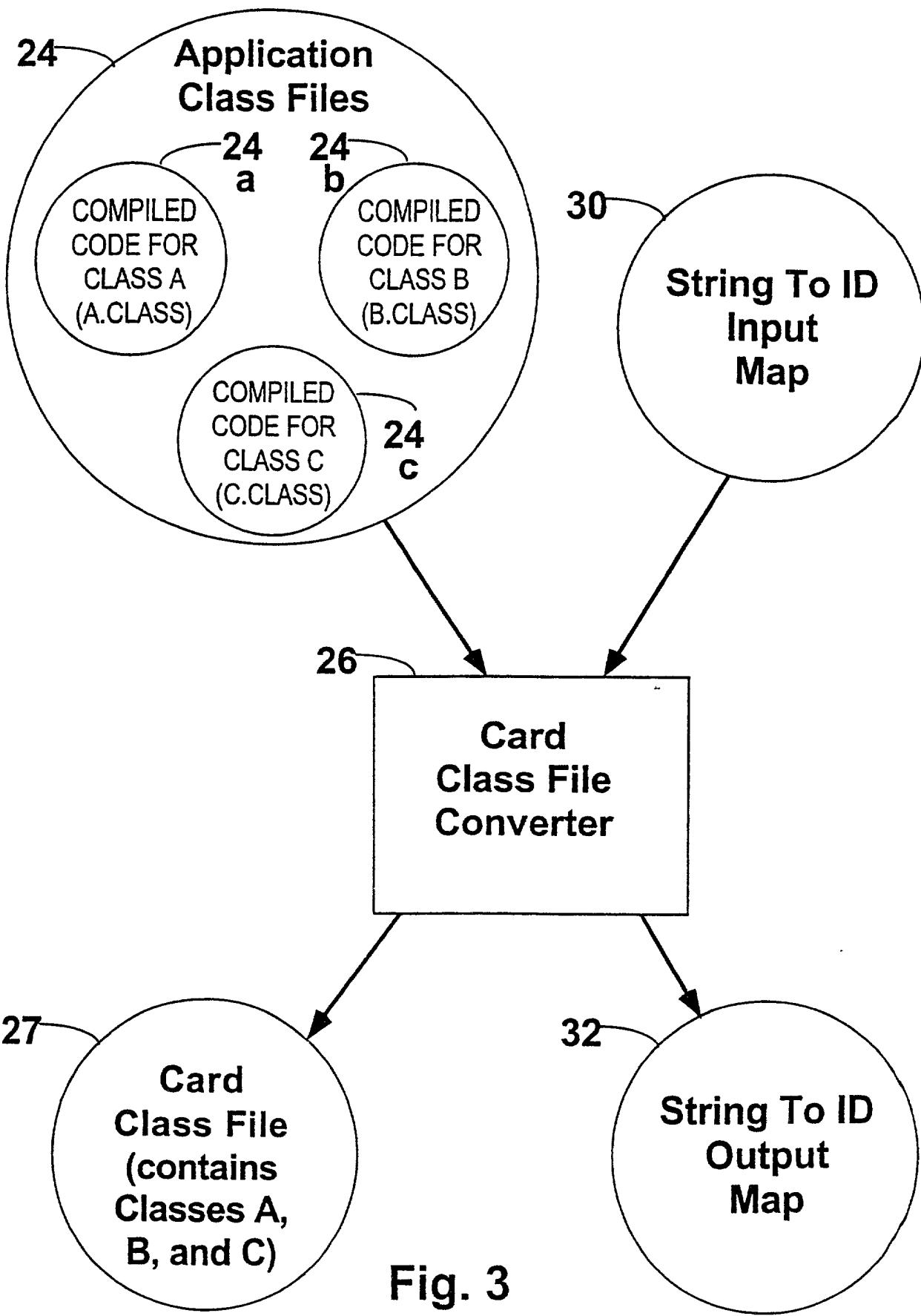


Fig. 3

Application Class Files

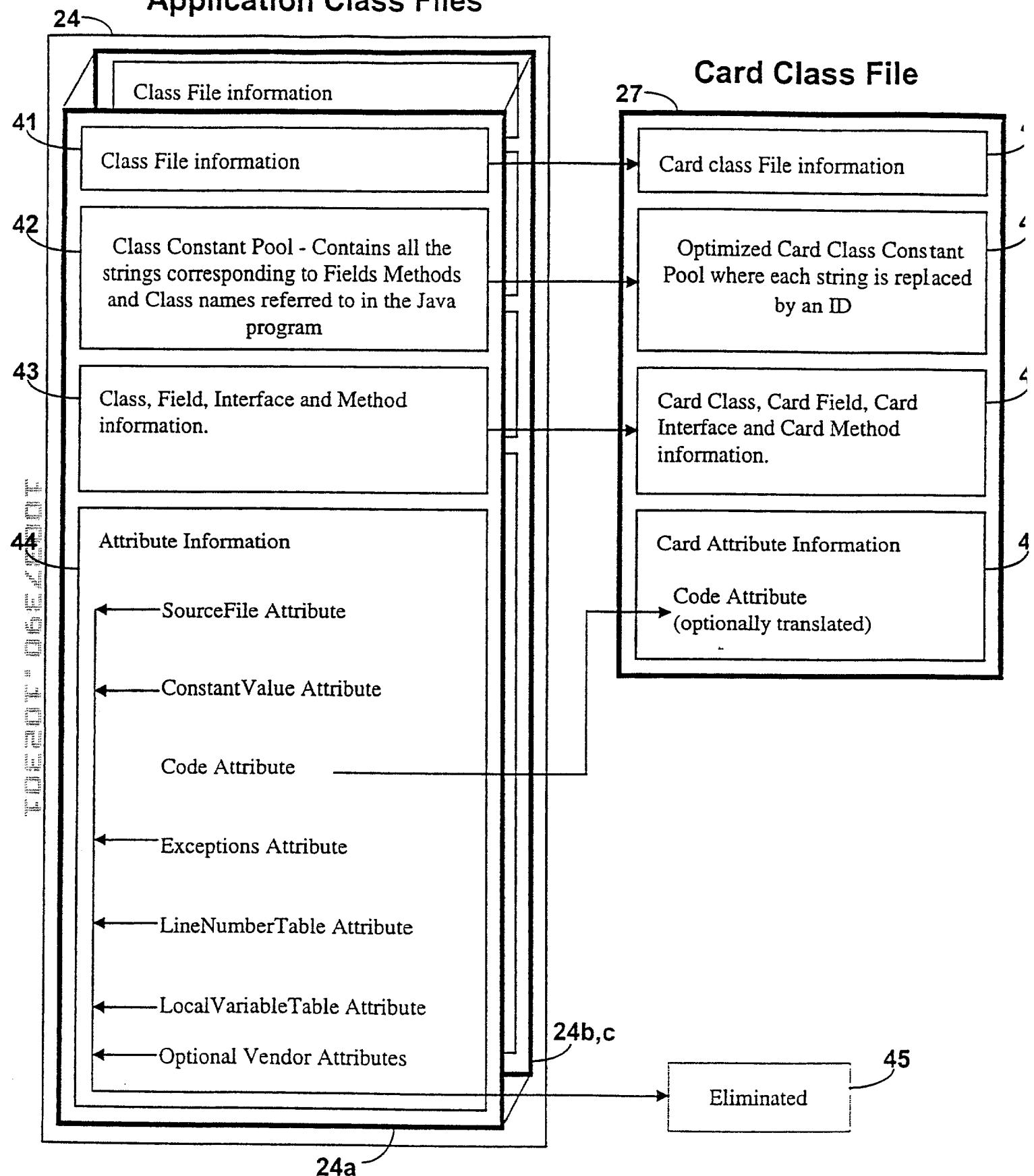


Fig. 4

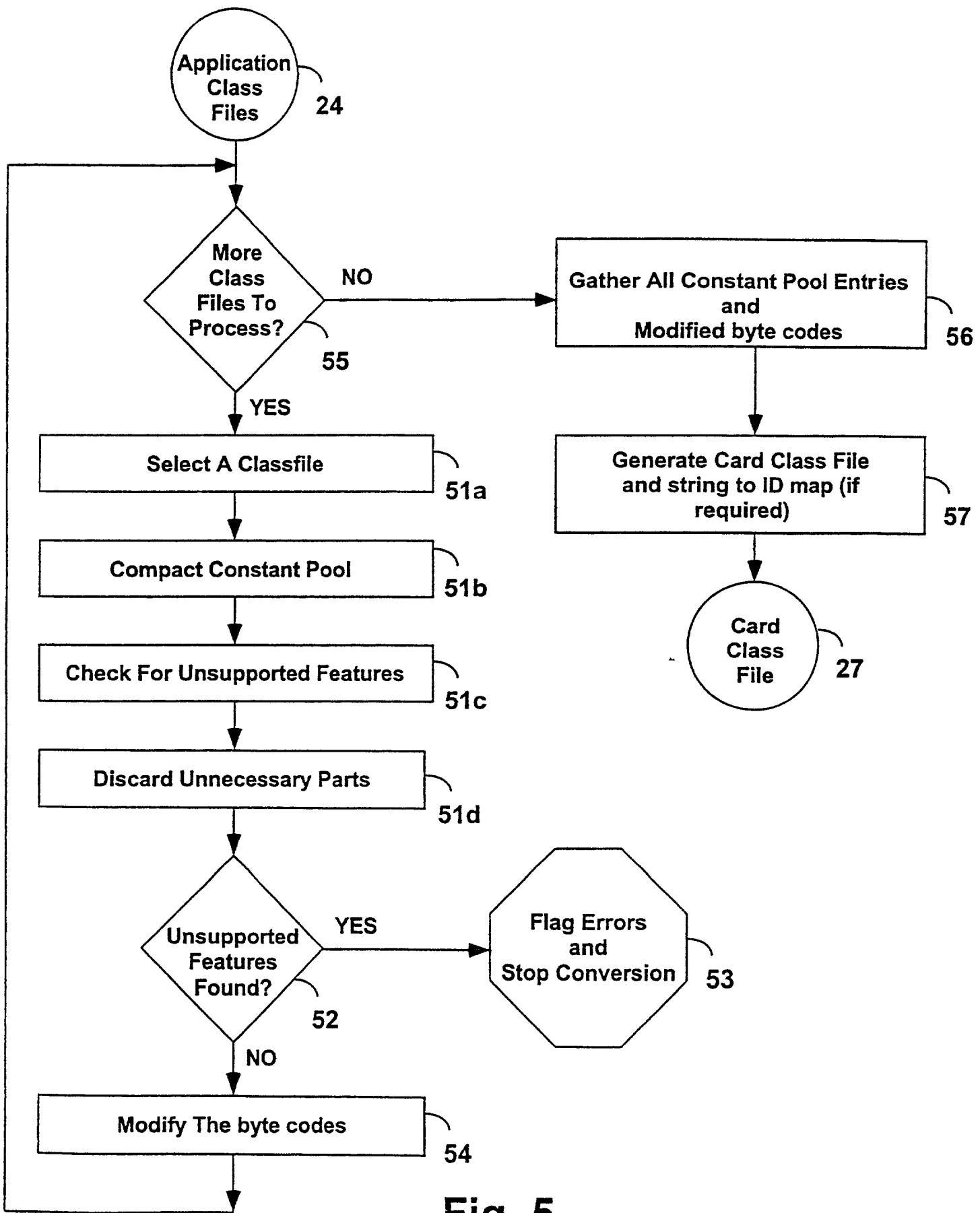


Fig. 5

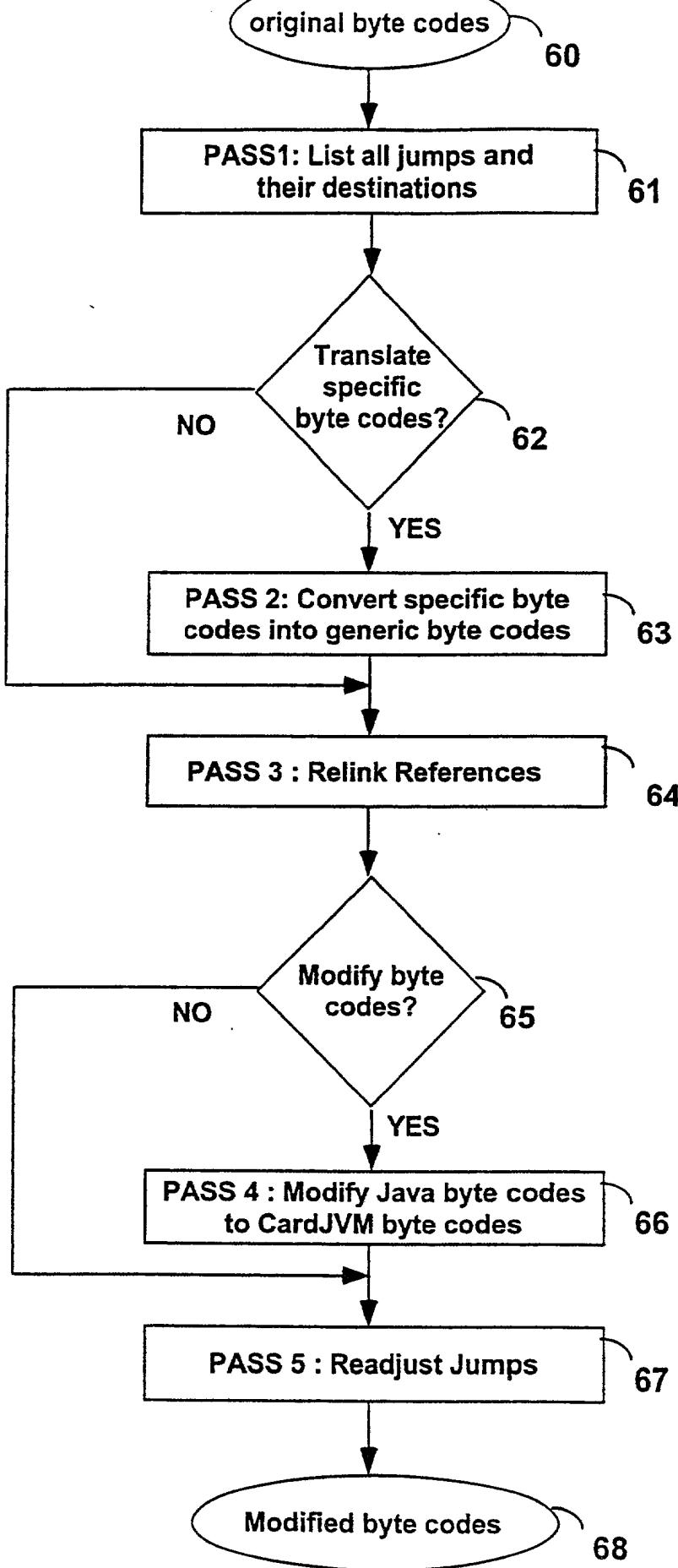


Fig. 6

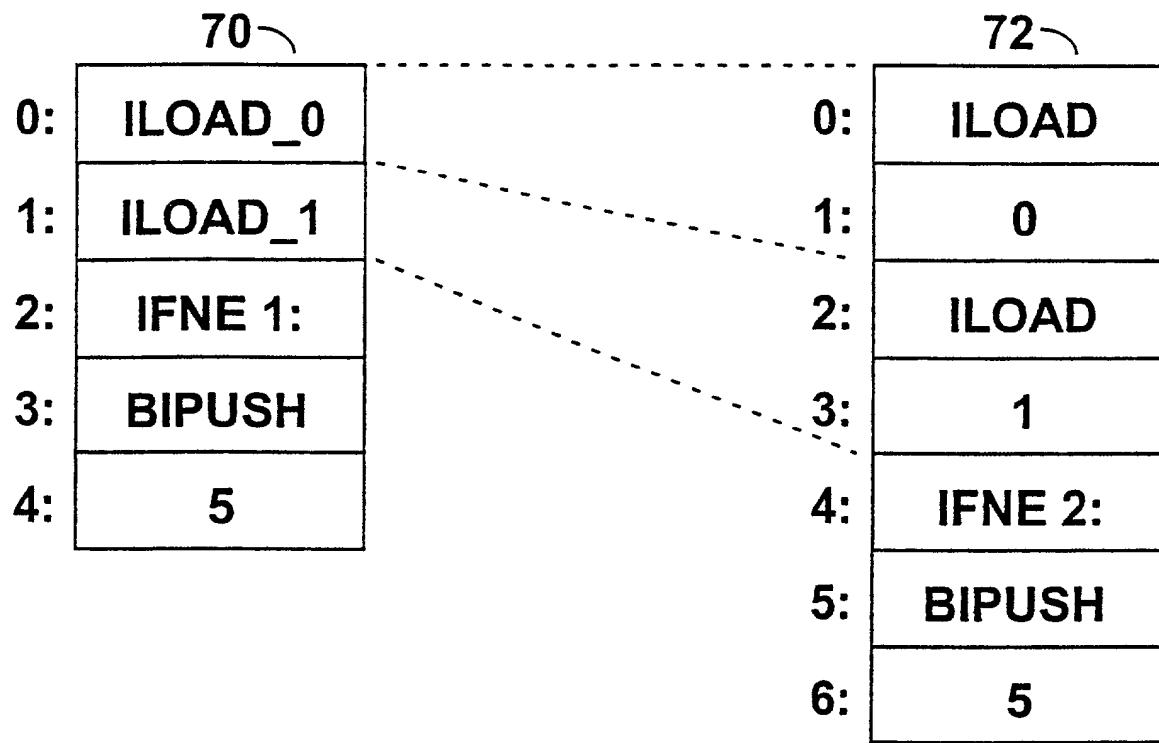


Fig. 7

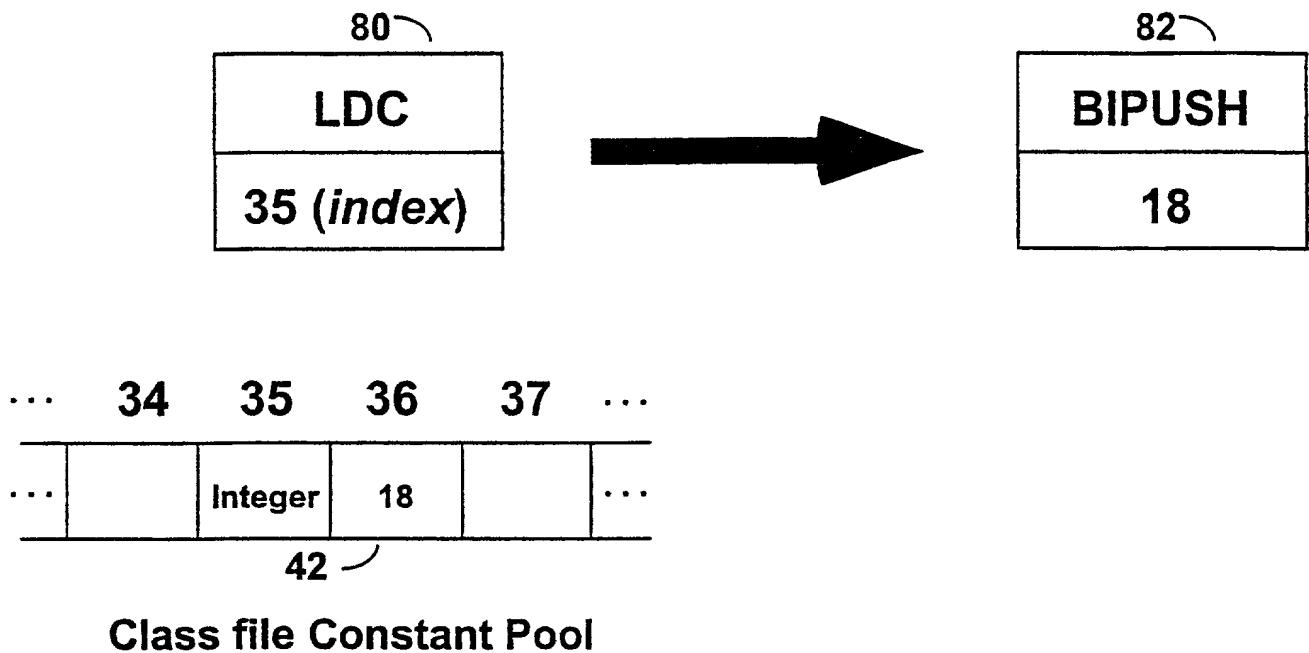


Fig. 8

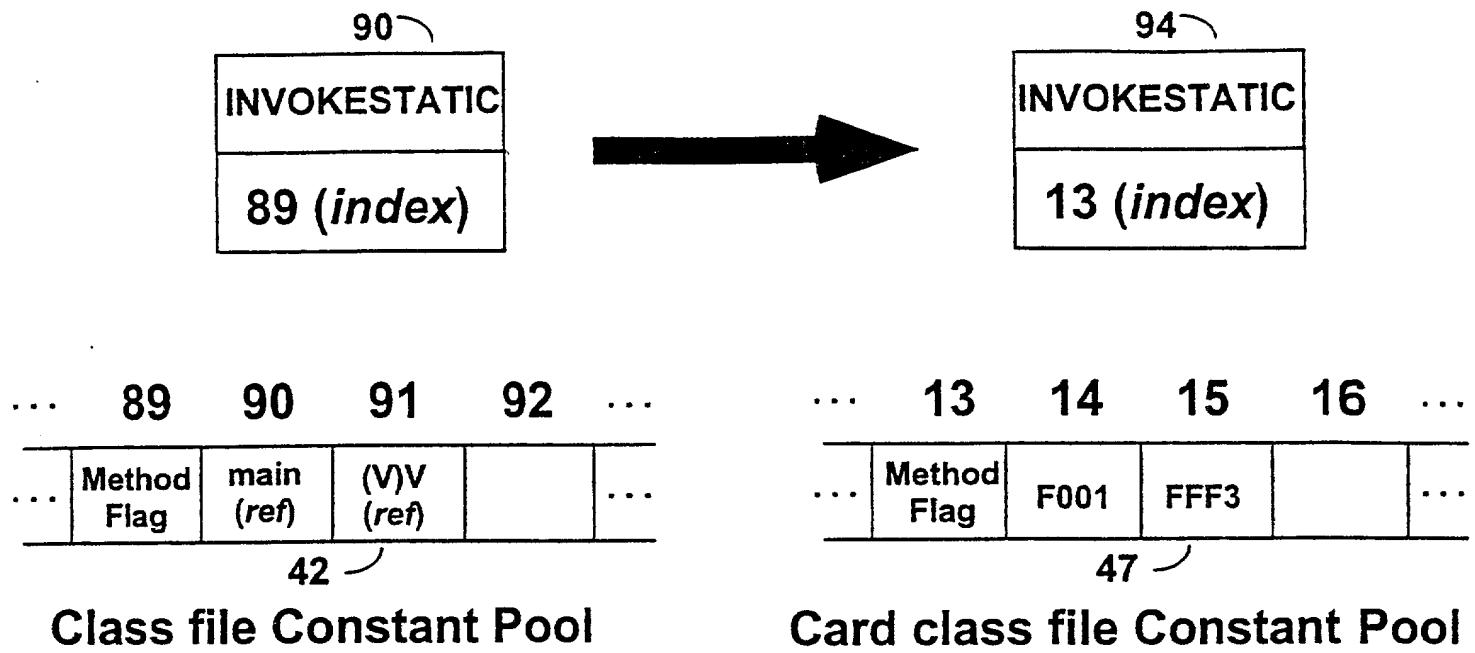


Fig. 9

100 ↗

| | |
|----|-------------|
| 0: | ALOAD 43 |
| 1: | 0 |
| 2: | ILOAD 21 |
| 3: | 1 |
| 4: | IFNE 154 2: |
| 5: | BIPUSH 16 |
| 6: | 5 |

102 ↗

| | |
|----|------------|
| 0: | ALOAD 51 |
| 1: | 0 |
| 2: | ILOAD 50 |
| 3: | 1 |
| 4: | IFNE 27 2: |
| 5: | BIPUSH 49 |
| 6: | 5 |

Fig. 10

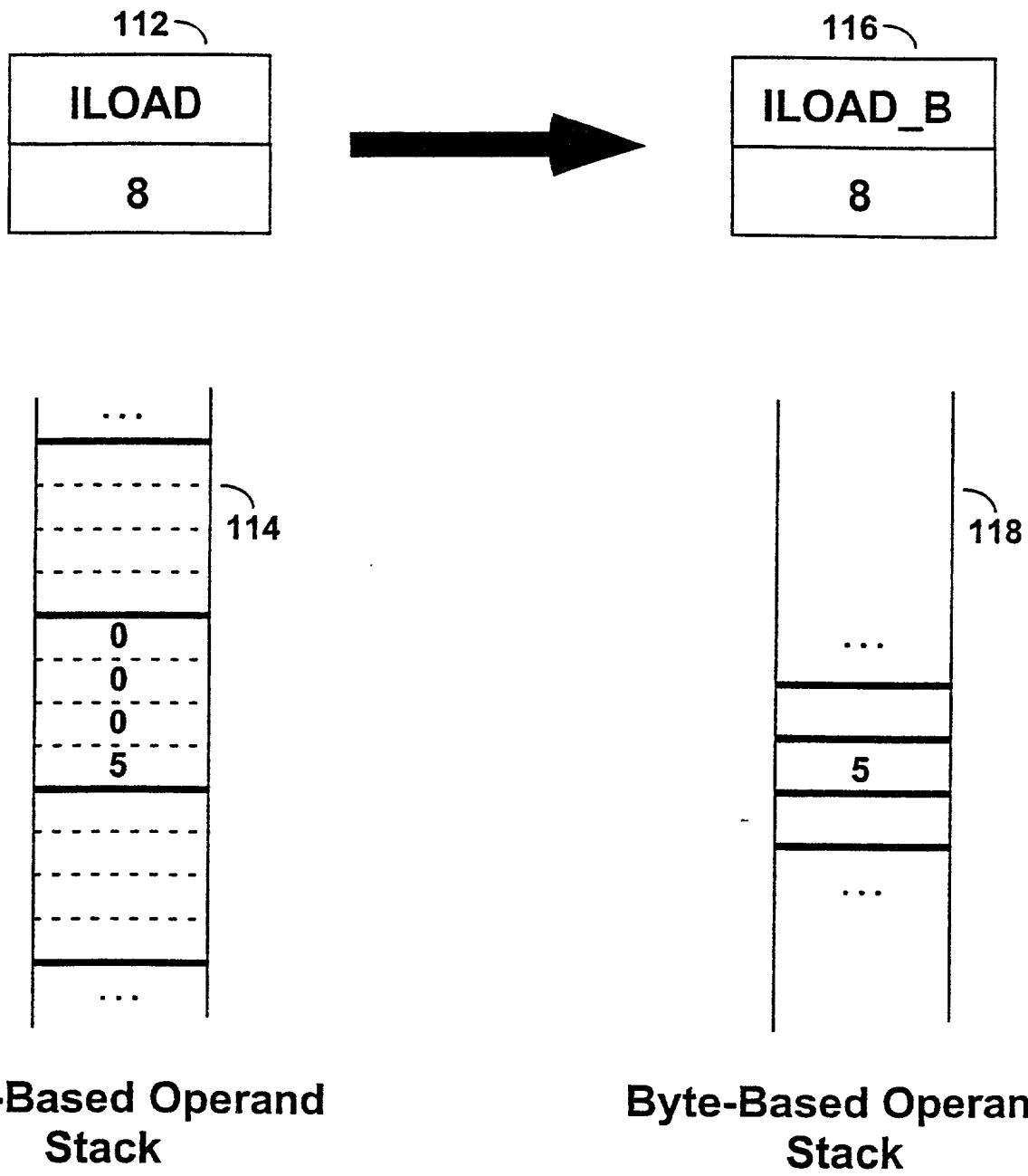
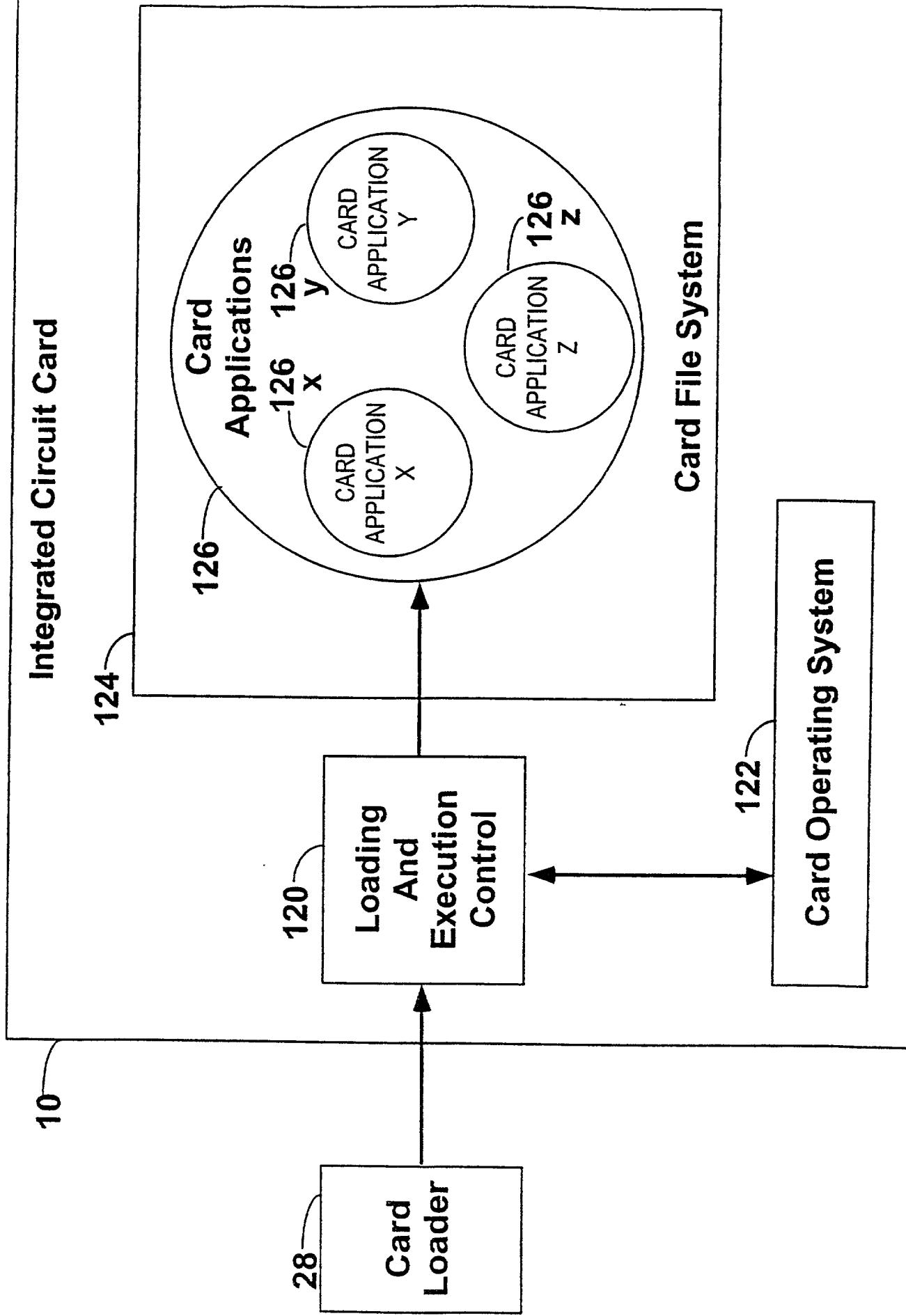


Fig. 11



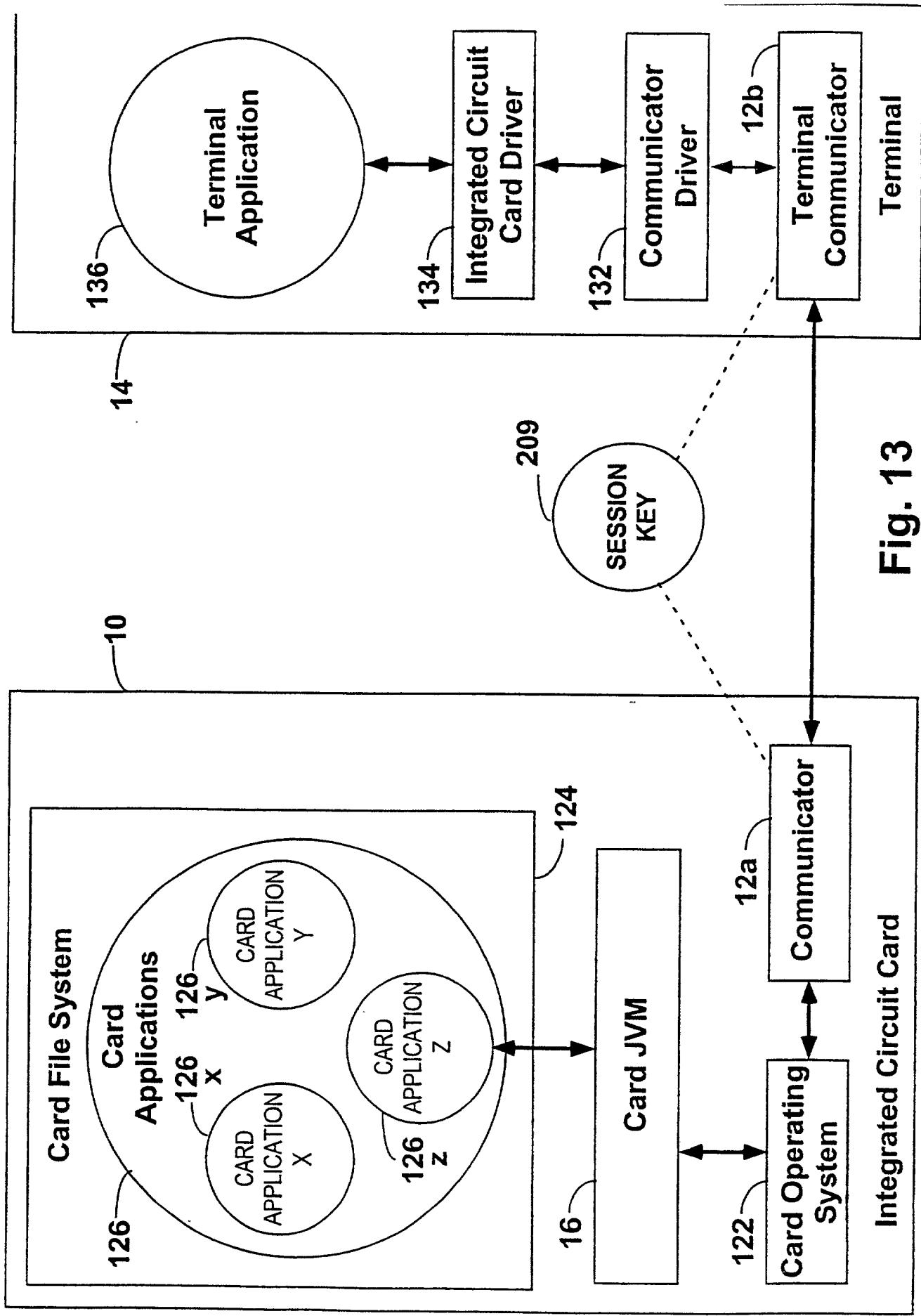


Fig. 13

Integrated Circuit Card

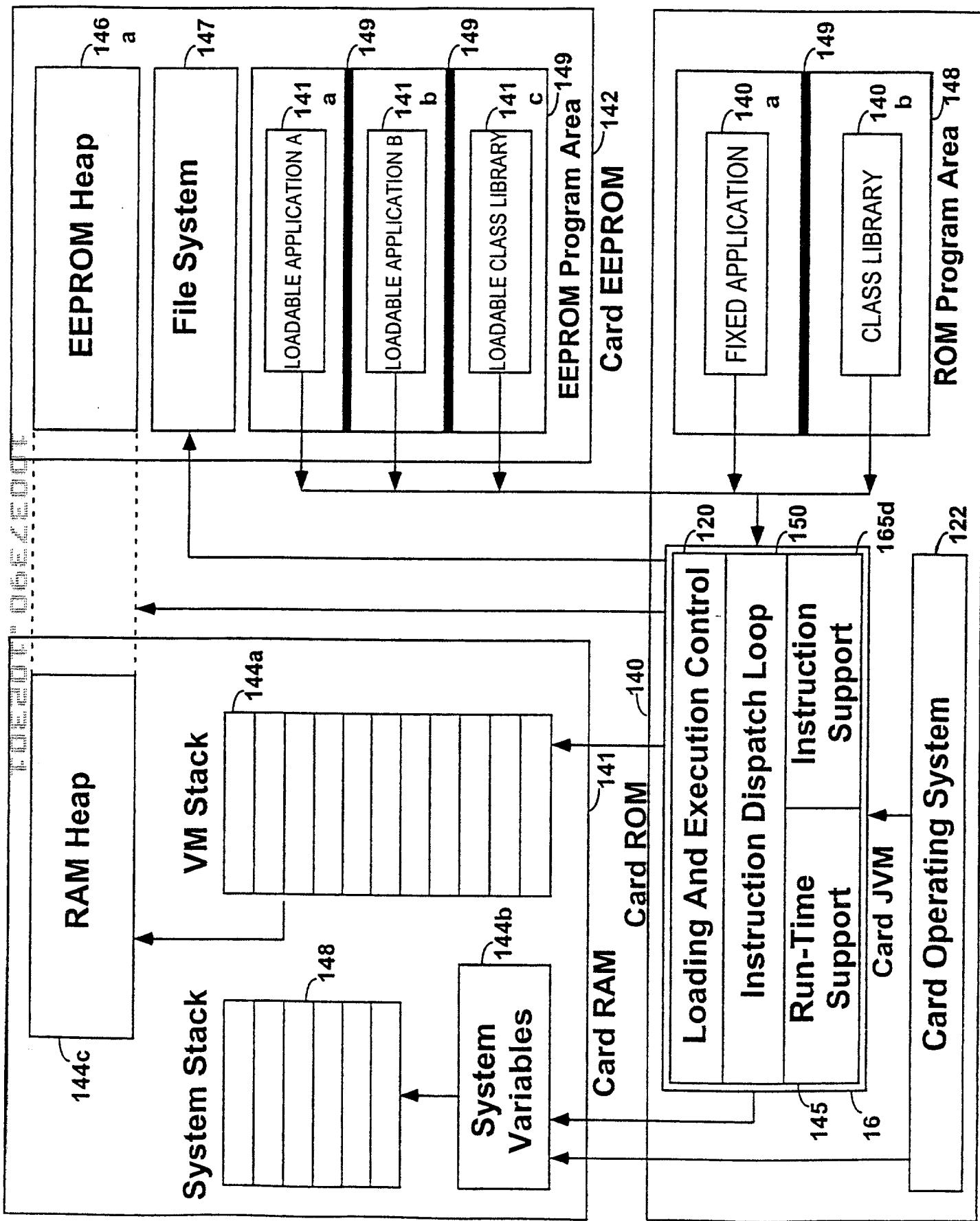


FIG.14

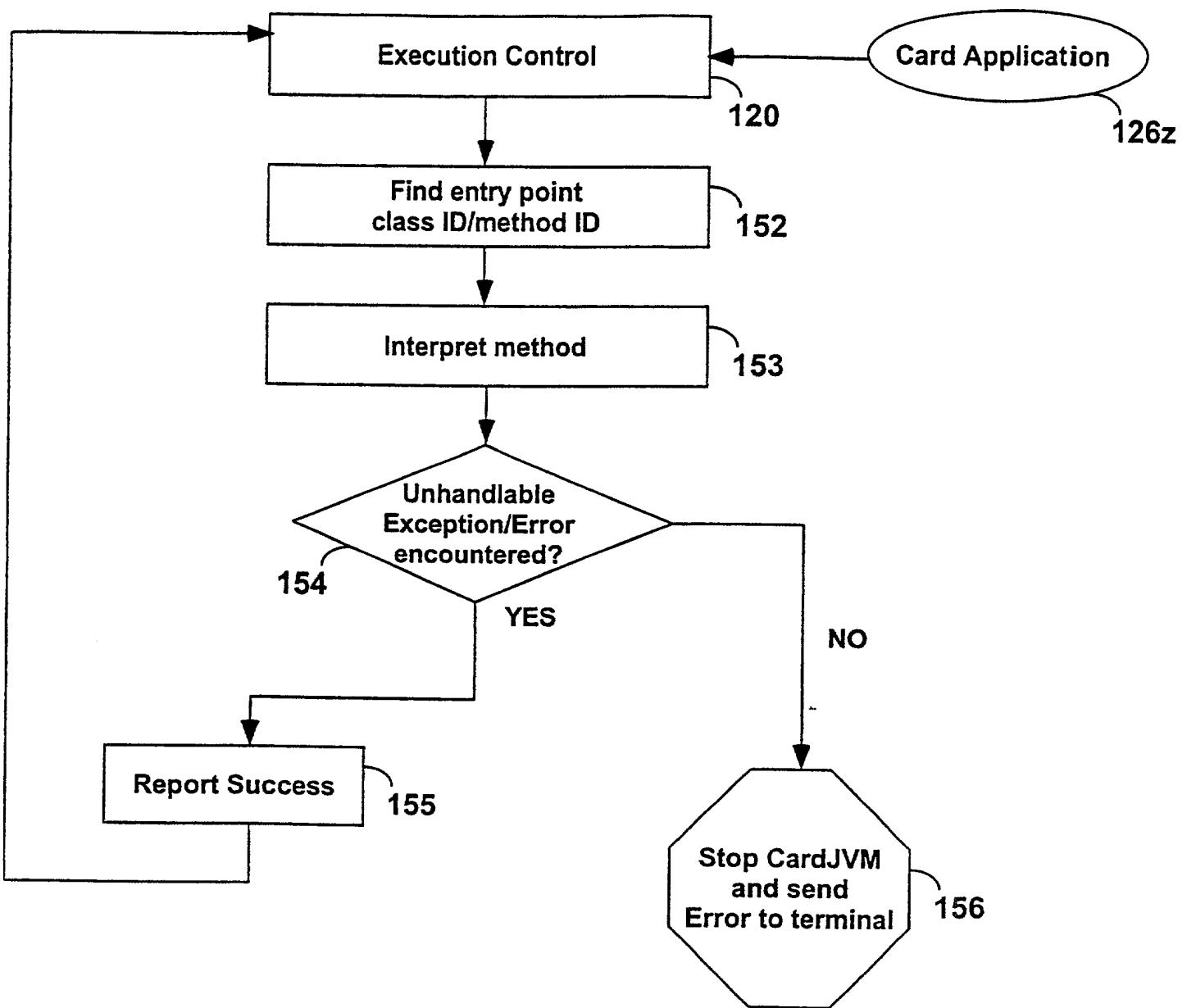


Fig. 15

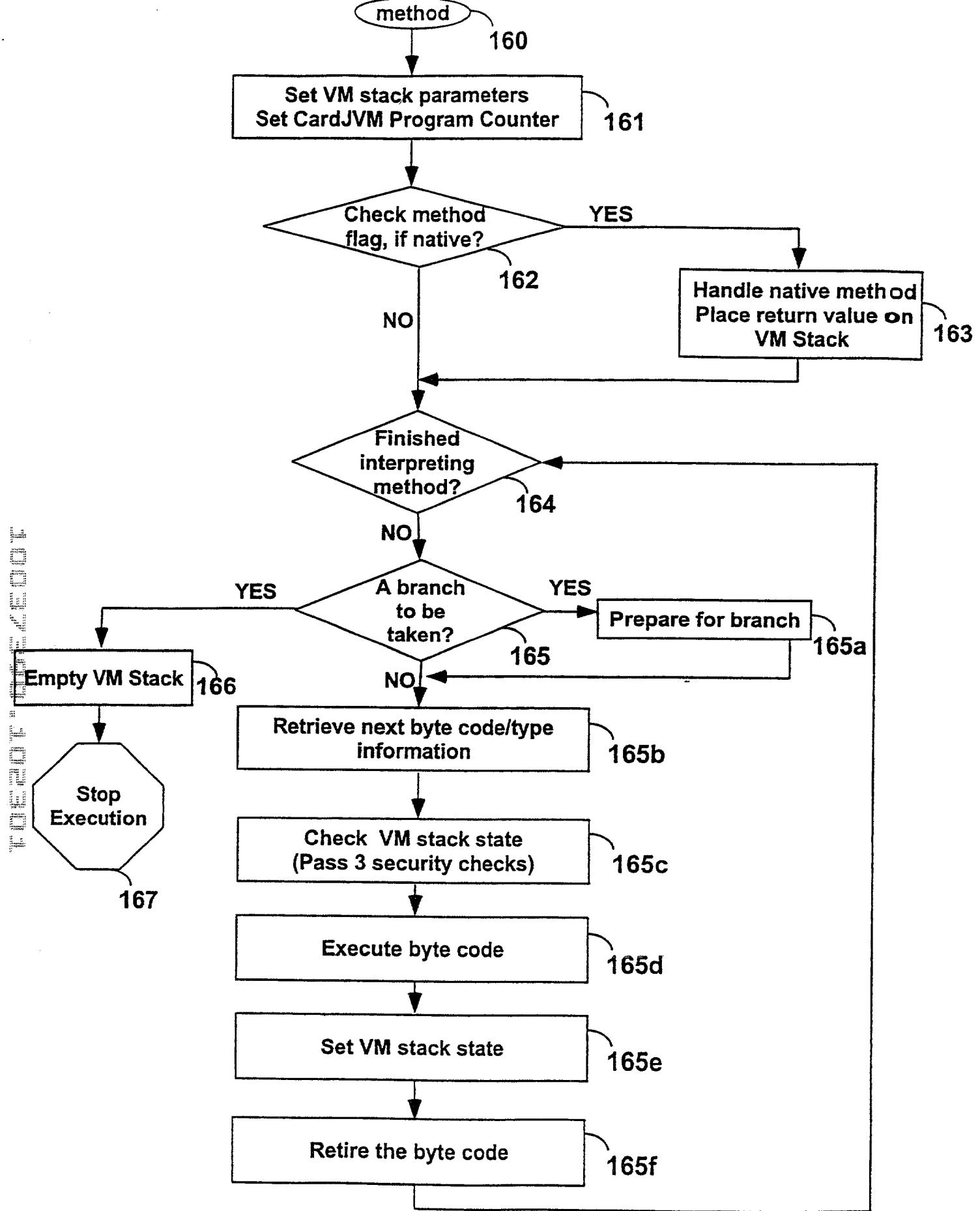


Fig. 16

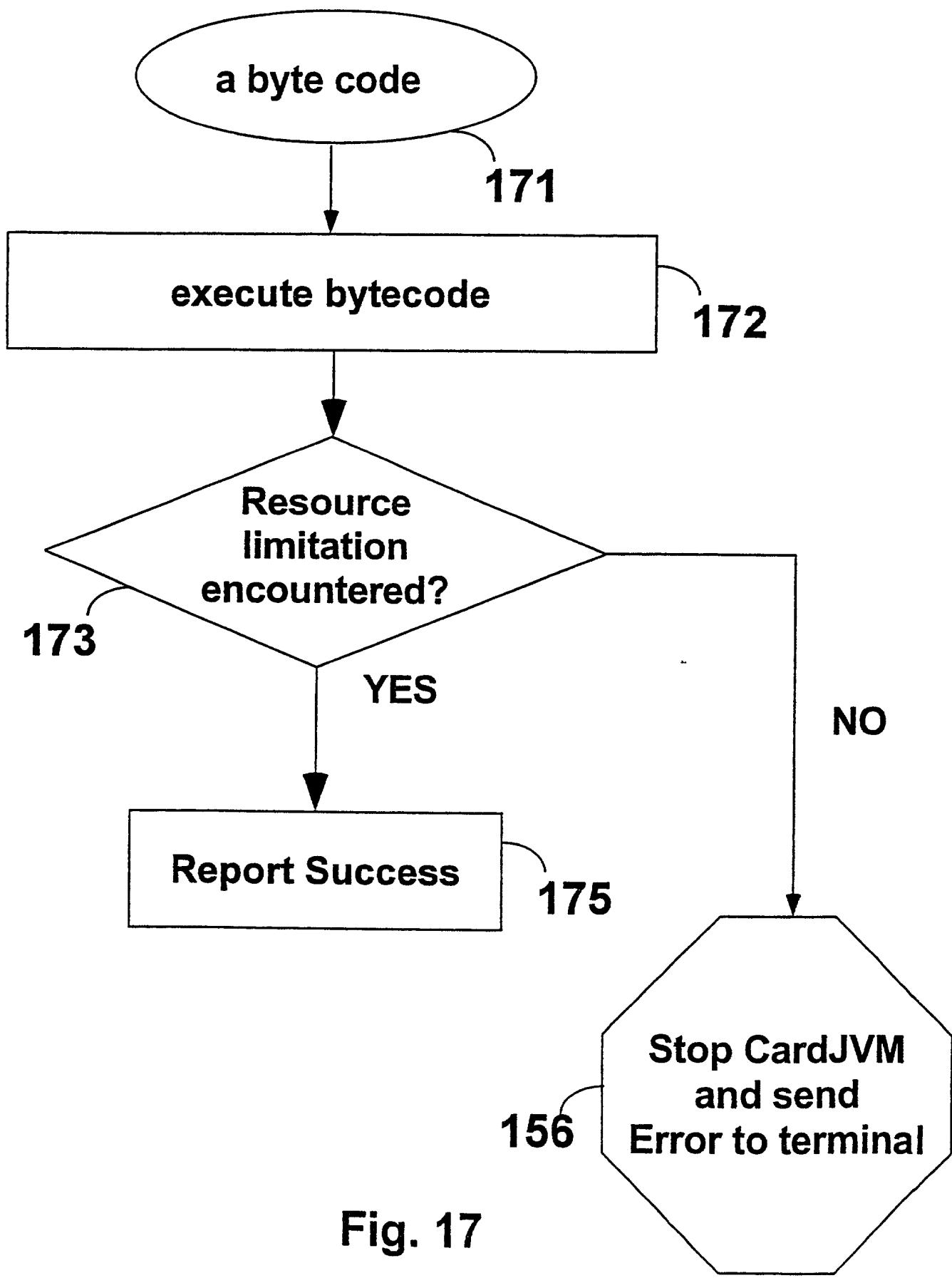


Fig. 17

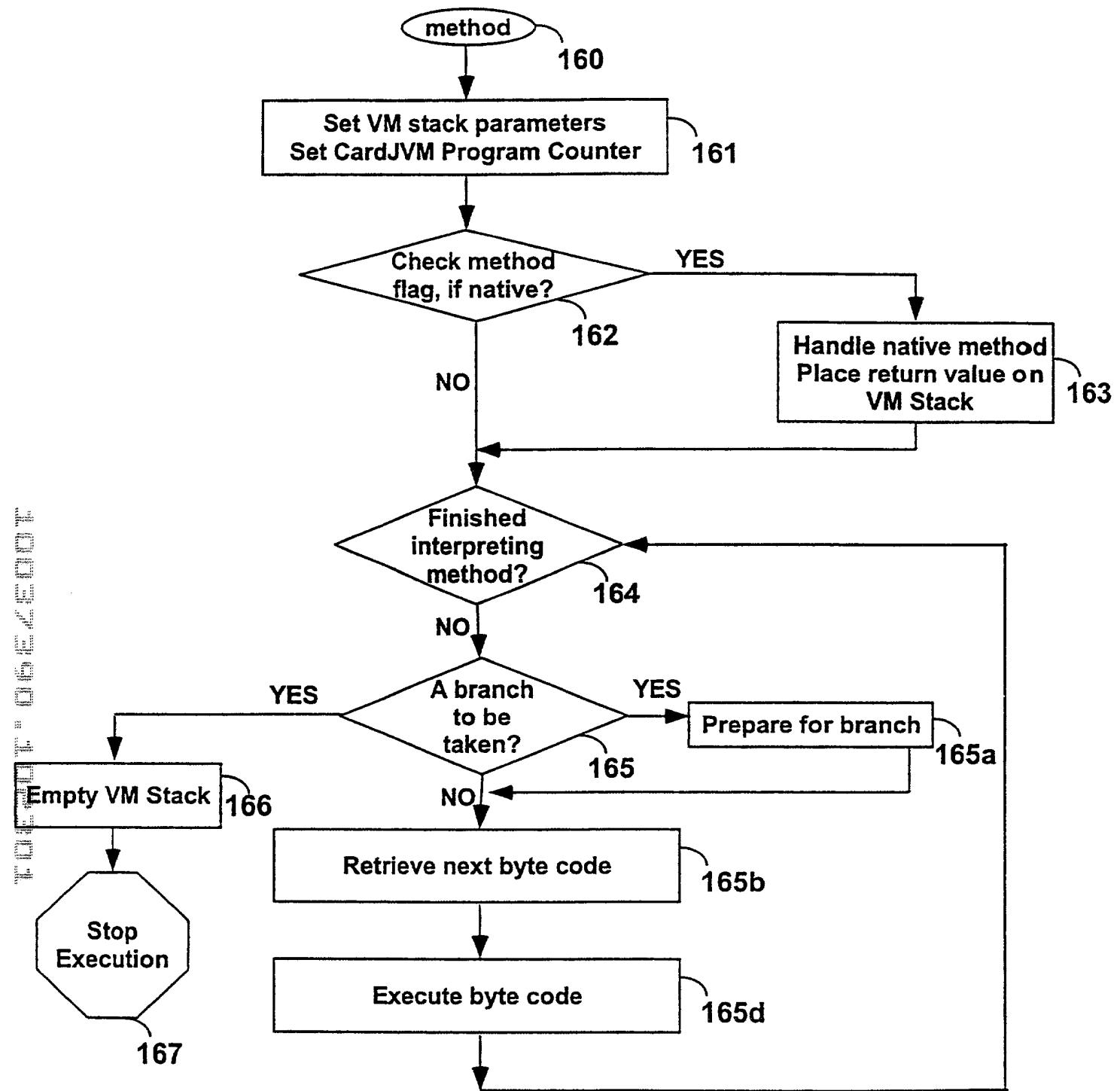


Fig. 18

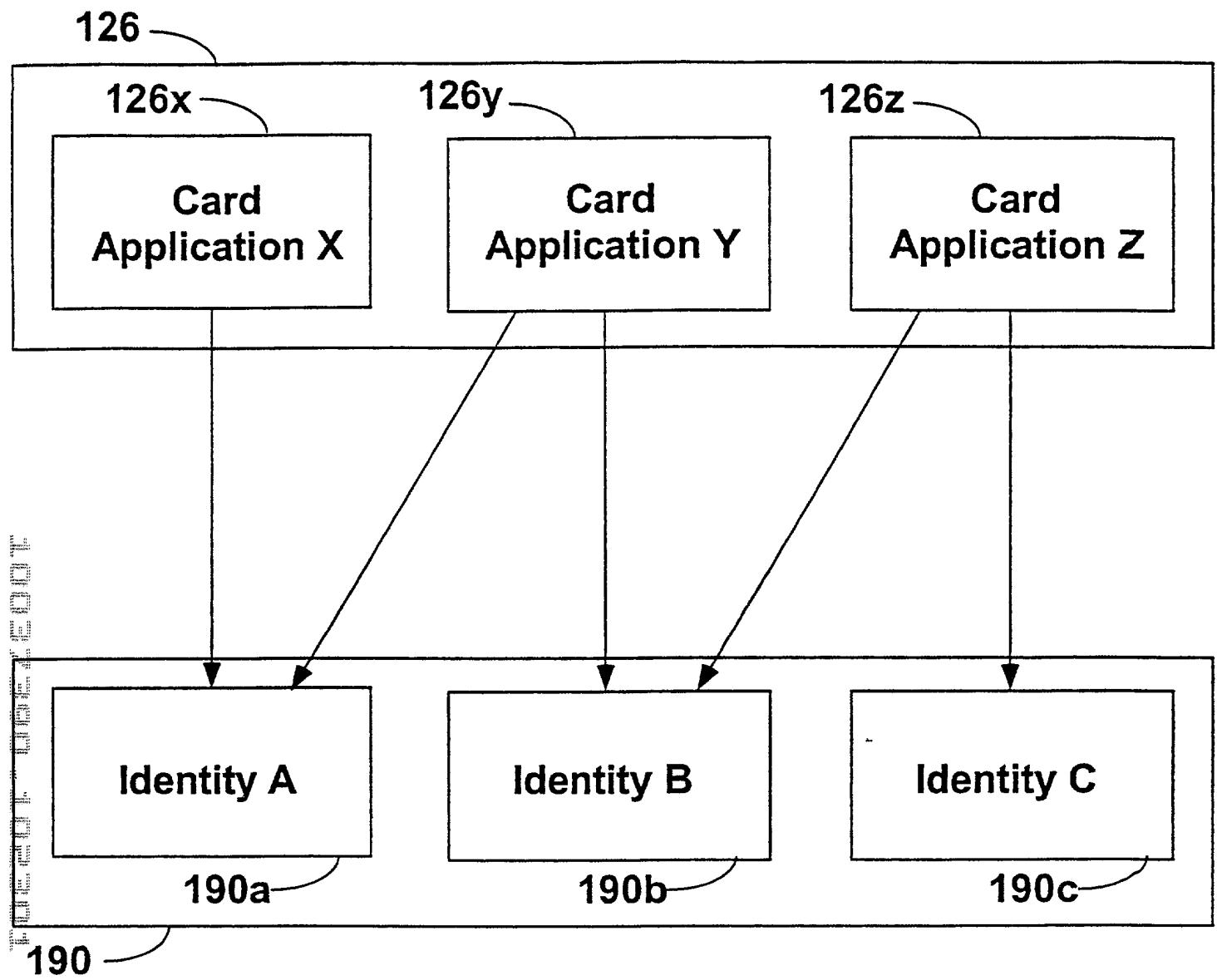


Fig. 19

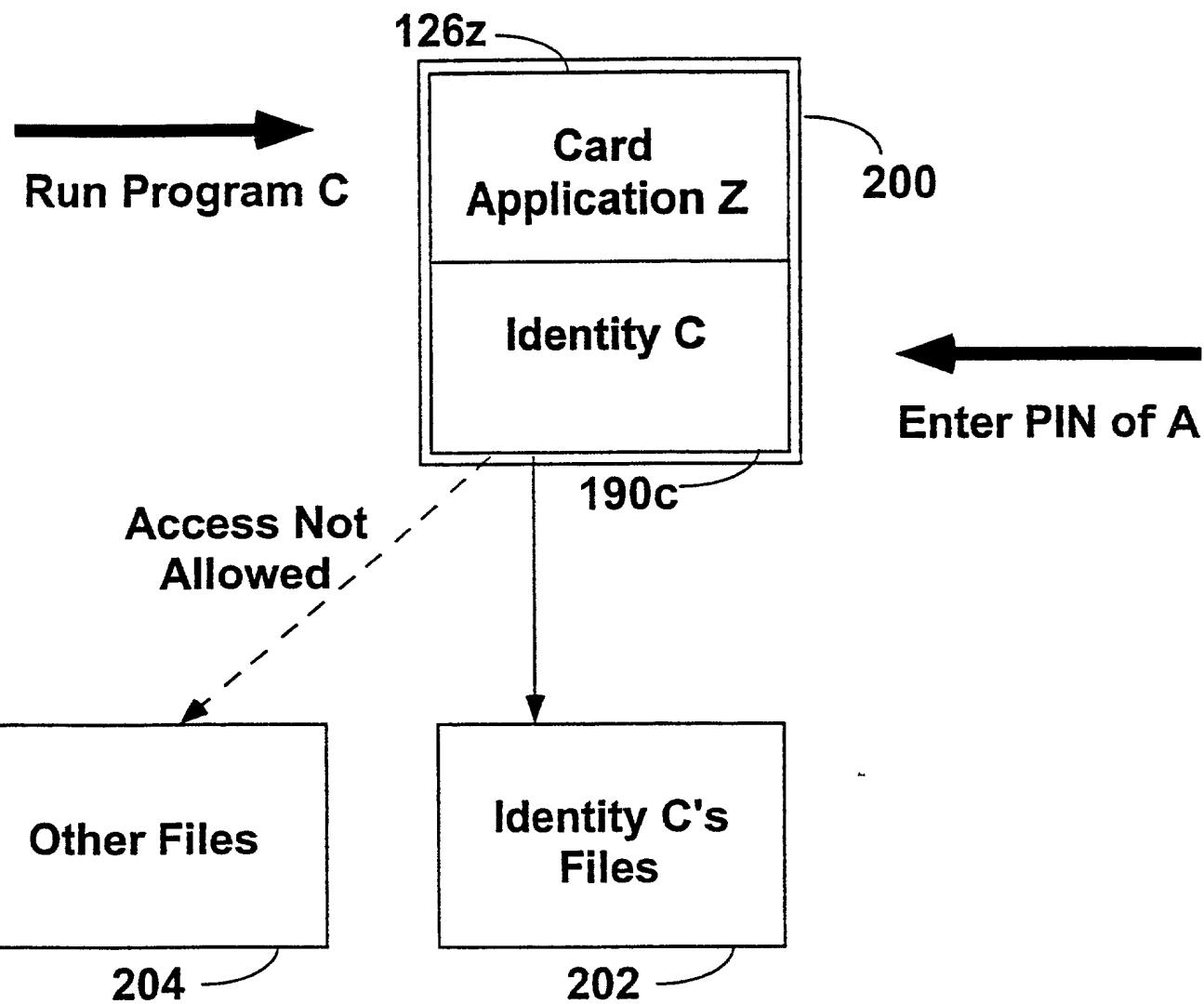


Fig. 20

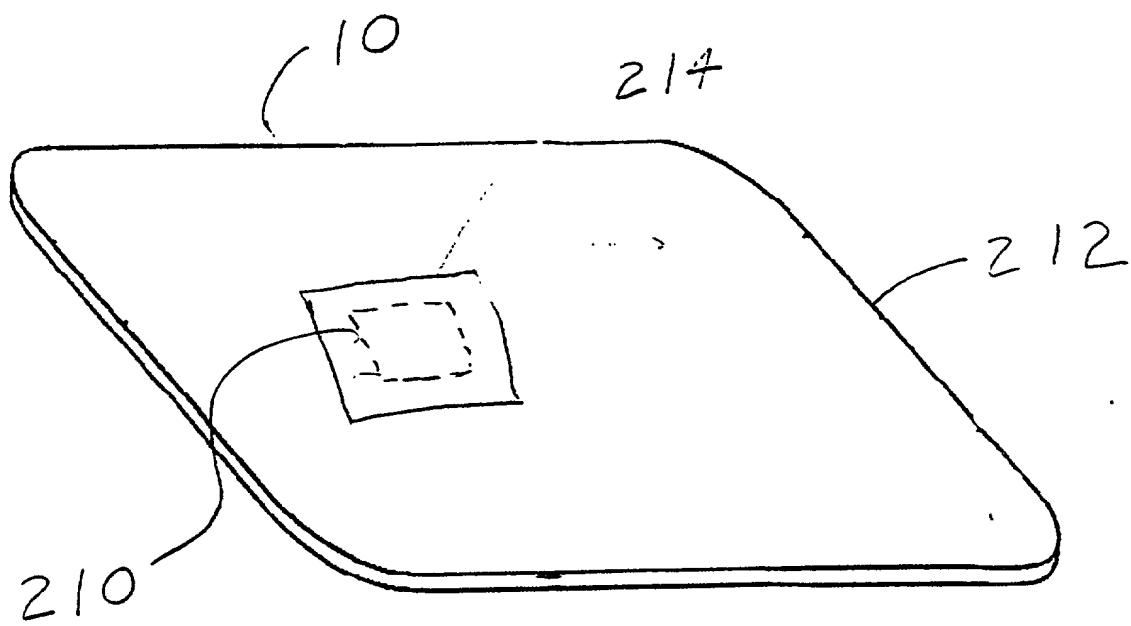


Fig. 21

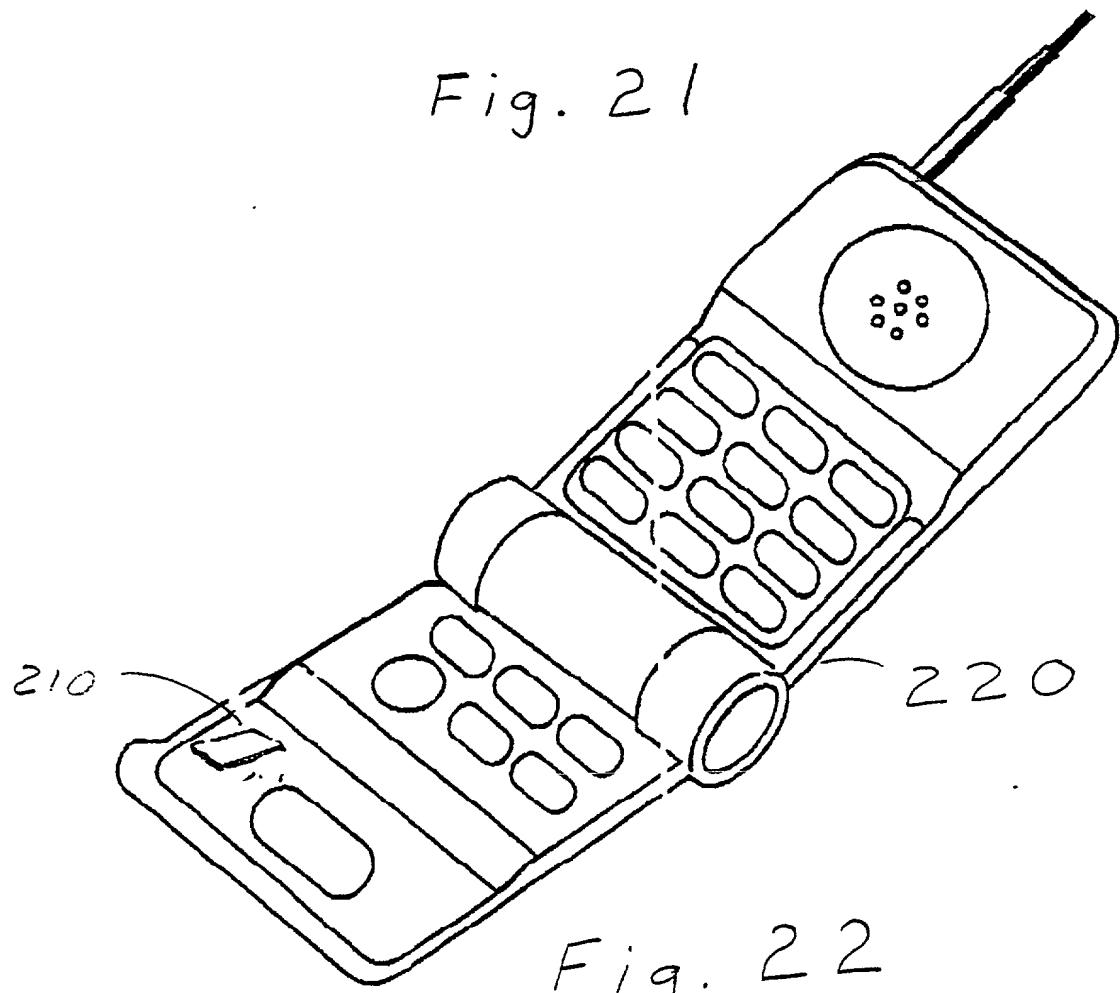


Fig. 22

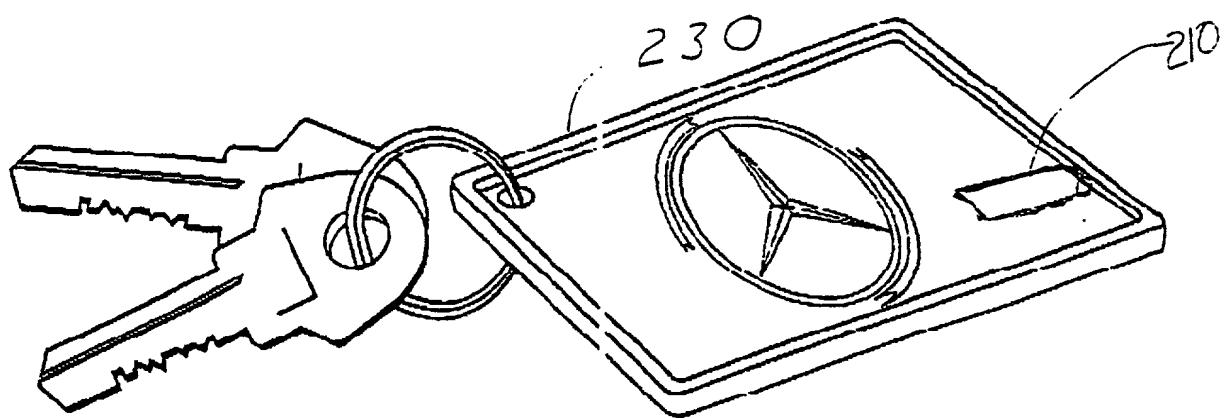


Fig. 23

1000 2000 3000 4000 5000 6000 7000 8000 9000

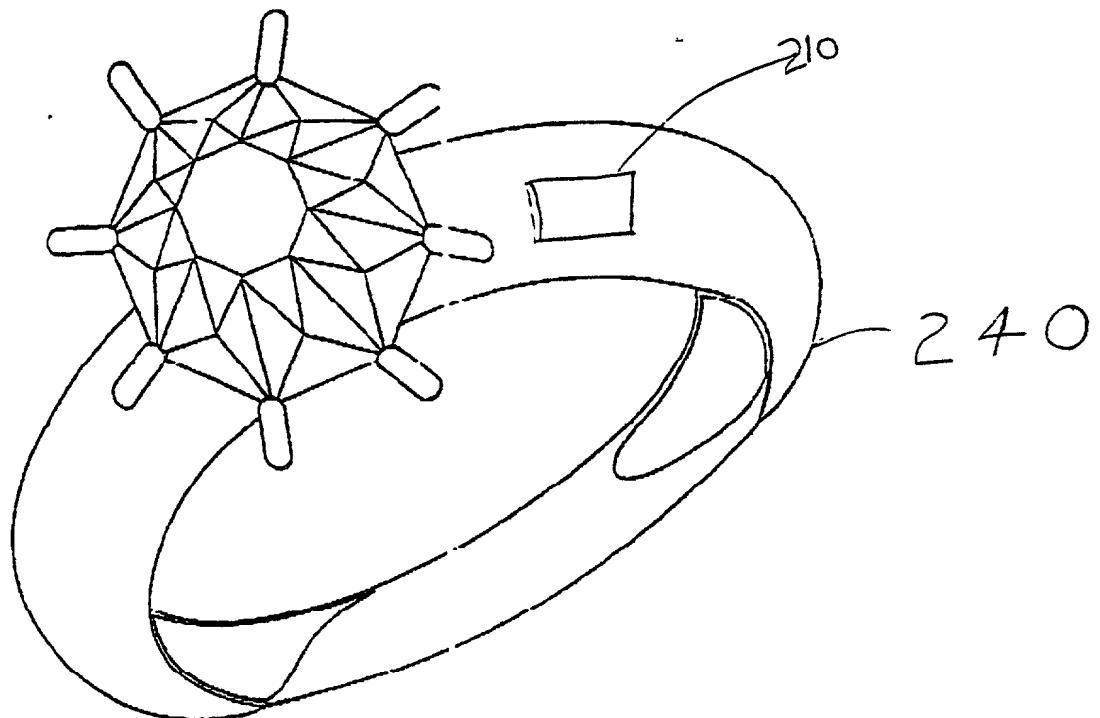


Fig. 24

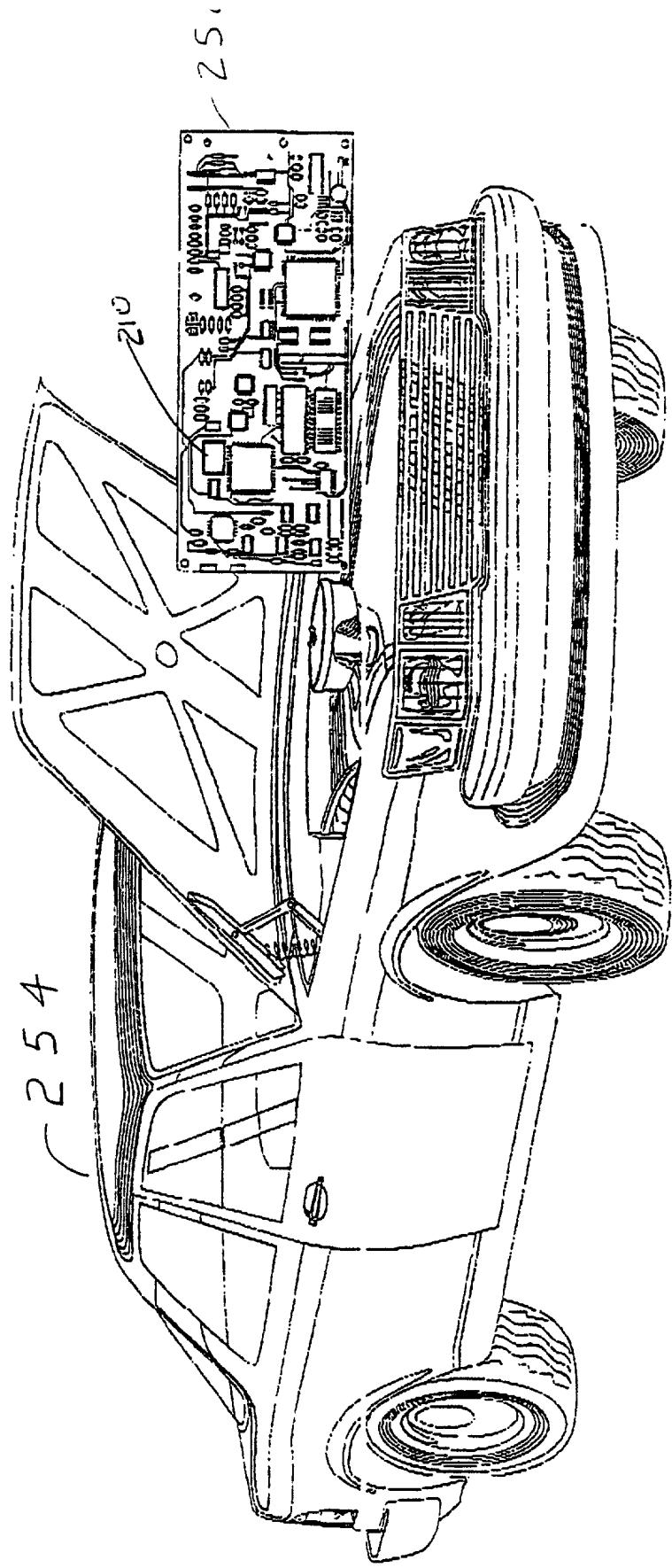


Fig. 25